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General Description

The E910.99 integrated circuit combines all required functions for a single chip Passive Infra Red (PIR) light controller.

It is designed for load switching with a transistor or a relay in 3 wire AC and DC systems.

A conventional PIR sensor connects directly to the PIR input. The pull-down resistor and DC decoupling circuitry are integrated on chip. The PIR signal is converted to a 15 bit digital value.

External potentiometers or resistors are used to set the operating parameters for sensitivity, on-time, brightness, fade, daylight sensor and environment temperature correction. The corresponding voltage levels are converted to digital values with a 4 bit resolution

All signal processing is performed digitally.

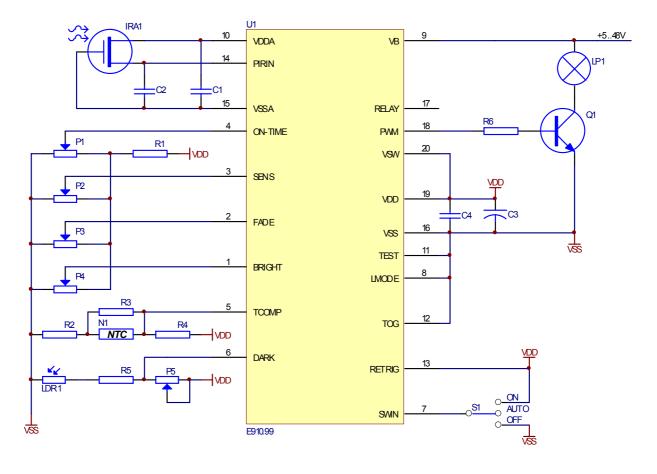
E910.99 PIR Light Controller for DC/AC Applications

Applications

- Battery operated lights
- Solar powered garden lights
- Outdoor and indoor motion sensor lights
- High end lighting switches
- Automatic bedroom night lights
- Energy saving

Features_

- Digital signal processing
- On chip supply regulator with wide operating voltage range
- Low power consumption
- Temperature compensation input
- Adjustable soft on/off switching (fading)
- Dimmer function
- PWM output
- Open Drain high voltage relay output
- Suitable for DC and AC applications



Application Circuit for a DC Sensor Light_





Electrical Characteristics

Absolute Maximum Ratings

Parameter	Min	Мах	Unit	Remarks
Voltage on pins VB, RELAY	-0.3	48	V	
Voltage on any other pin	-0.3	7	V	
Current into any pin	-100	100	mA	One pin at a time
Storage temperature	-45	125	°C	

Table 1: Electrical Characteristics (Stresses beyond those listed above may cause permanent damage to the device. Exposure to absolute maximum ratings may affect the device reliability. ESD protection: all pins will be able to withstand a discharge of a 100pF capacitor charged to 1.6kV through a 1500Ω series resistor. Test method: MIL-STD-883D method 3015).

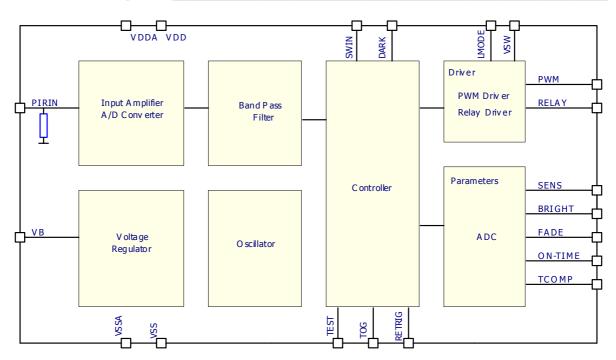
Operating Conditions (T=25°C, VDD=5V, unless stated otherwise)

Parameter	Min	Тур	Max	Unit	Remarks
Temperature					
Operating temperature range	-25		70	°C	
Regulator					
Supply voltage VB	5		48	V	Supply voltage
Supply current			130	μA	V _B =12V (Outputs unloaded)
Regulator output voltage	3.6	4	4.4	V	
Digital Inputs, Schmitt Triggers (RETRIG, DAR	K, LMOD	E, SWIN	, TEST,	TOG, VS	W)
Input low voltage			20	$%V_{DD}$	
Input high voltage	80			%V _{DD}	
Input low voltage VSW			70	%V _{DD}	
Input high voltage VSW	90			%V _{DD}	
Pull down current on TEST, TOG		140		μA	input to V _{DD}
Pull up current on RETRIG		140		μA	input to V _{SS}
Leakage current on DARK, LMODE			±1	μA	input to V_{SS} or V_{DD}
Pull up/ pull down scanning current on SWIN			300	μA	input to V _{SS} or V _{DD}
Digital Outputs		•		•	
RELAY sink capability (open drain)	20			mA	V _{OL} <1V
PWM Output low voltage			10	%V _{DD}	I _{SOURCE} =5mA
PWM Output high voltage	90			%V _{DD}	I _{SINK} =2mA
Analog Inputs		• •			
Input leakage current (ON-TIME, SENS, FADE, BRIGHT, TCOMP)	-1		1	μA	
PIRIN resistance to V _{SS}		70		kΩ	
PIRIN input AC voltage		100		mV	Peak-to-Peak
PIRIN input DC voltage	V _{SS}		V _{DD}	V	
PIRIN resolution	11	14	17	μV	Per threshold count
Oscillator and Filter					
LPF cutoff frequency		7		Hz	
HPF cutoff frequency		0.44		Hz	
Clock frequency		64		kHz	
Table 2: One retires Constitions	1	I		I	

Table 2: Operating Conditions



Detailed Description



Voltage Regulator

The on-chip series regulator can accept a large variety of supply voltages, and generates a stable 4V supply for the internal circuitry. The V_{DD} pin requires a decoupling capacitor to V_{SS} .

Oscillator

The IC contains an on-chip low power oscillator. The frequency is set to 64kHz by connecting the correct resistor between CLOCK and VDD. The timing signals and cutoff frequencies of the digital filters are derived from this frequency.

PIR Sensor Input

The PIRIN input has an internal pull-down resistor. The analog to digital converter generates a digital signal from the voltage level measured on the PIRIN pin.

Band-Pass Filter

A 2^{nd} order low-pass filter with a cut-off frequency of 7 Hz eliminates unwanted higher frequency components. This signal is then passed to a 2^{nd} order high pass filter with a 0.44Hz cut-off frequency.

Parameter Settings

5 different parameters can be set by changing the voltages on these pins between V_{SS} and $V_{\text{DD}}\text{*}0.231$

SENS: Sets the sensitivity threshold required to generate a trigger condition. Refer to table 3. One count represents $14\mu V PIR$ voltage.

BRIGHT: Sets the maximum brightness of the light connected to the PWM output. The brightness levels are divided into 15 steps. Maximum brightness occurs at a pin voltage of 0.231*VDD.

FADE: Sets the time it takes to switch the light on or off (soft dimming). Refer to table 3.

ON-TIME: Sets the time for the light to remain on. Refer to table 3.

ldeal voltage for value selection	On time (s)	Fade time (s)	SENS threshold Voltage	SENS threshold counts
Above 0.224 x VDD	2048	4.0	1.792mV	128
0.194 x VDD	1024	3.5	896 µV	64
0.164 x VDD	512	3.0	448 µV	32
0.134 x VDD	256	2.5	224 µV	16
0.105 x VDD	128	2.0	112 µV	8
0.076 x VDD	64	1.5	56 µV	4
0.045 x VDD	32	1.0	28 µV	2
Below 0.018 x VDD	8	0.5	14 µV	1

Table 3: On-time and Fade-time

TCOMP: Temperature compensation input pin. Α temperature dependent resistor network may be connected to this pin to generate voltages between 0.113*VDD and 0.231*VDD. The voltage on this pin must decrease as the temperature increases. At 37°C, the voltage should be between 0.142*VDD and 0.149*VDD. Internally, a TCOMP factor is selected, based on this pin voltage. This TCOMP factor is multiplied with the sensitivity threshold. Table 4 shows the relationship between TCOMP pin voltage and the TCOMP factor used by the threshold comparator. The TCOMP input may be connected to VSS if the application does not require a temperature compensation function.



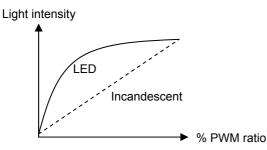
Pin voltage on TCOMP	TCOMP factor	Pin voltage on TCOMP	TCOMP Factor
Below 0.124 x VDD	7/8	0.180 x VDD	8/8
0.127 x VDD	6/8	0.187 x VDD	9/8
0.135 x VDD	5/8	0.195 x VDD	10/8
0.142 x VDD	4/8	0.202 x VDD	11/8
0.150 x VDD	4/8	0.210 x VDD	12/8
0.157 x VDD	5/8	0.217 x VDD	13/8
0.165 x VDD	6/8	0.225 x VDD	14/8
0.172 x VDD	7/8	Above 0.234 x VDD	15/8

Table 4: Temperature compensation factor

PWM and RELAY output drivers

The load can be switched ON and OFF with a relay, using the RELAY open collector output.

When fading and brightness control are desired, the PWM output must be used. An external transistor is normally required to switch the load current. The light intensity versus PWM ratio is different for an LED than an incandescent light, as indicated in the figure below.



For this reason, the PWM steps are different when setting the LMODE high (for LED's) or low (for incandescent).

VSW input

The VSW input may be used to sense a slow charging relay supply voltage before enabling the outputs of the device. The RELAY and PWM outputs becomes active when the VSW > 0.9*VDD. RELAY and PWM outputs becomes inactive when the VSW < 0.7*VDD.

Controller: Modes of Operation

The operating modes are determined by four digital inputs. The digital inputs can be connected to V_{DD} , V_{SS} or some can be left floating, as indicated below.

Pin Name	Description
SWIN	Selects the ON-AUTO-OFF mode. V_{DD} : light permanently ON V_{SS} : light permanently OFF Floating: PIR sensor mode (AUTO)

RETRIG	Re-trigger Mode
	V _{DD} or floating: The timer for the on-
	time is restarted whenever movement
	is detected.
	V _{SS} : The light will stay on for the on-
	time. Movement detection is ignored
	during this period.
DARK ¹	Typically connected to a Light
	Dependant Resistor (LDR) or photo
	transistor, to prevent the light from
	switching on during daylight conditions.
	SCHMITT Trigger input
	DARK > 3/4 VDD indicates "dark" thus
	light switching is enabled
	DARK < 1/4 VDD indicated "not dark"
	thus light switching is not enabled.
	Do not leave this input floating.

Table 5: Operational parameters

¹ DARK is ignored when the light is ON in PIR sensor mode (AUTO).

Operation_

Power-up Mode

Whenever the circuit is powered up, the light is switched on for the selected on-time duration. The DARK input is ignored on power-up, to allow the user to verify the installation during daylight conditions.

Trigger condition

The SENS threshold (refer to table 3) is multiplied with the TCOMP factor (refer to table 4), to obtain a temperature dependent threshold. When the filtered PIR signal exceeds this threshold, a trigger condition occurs.

Conditions for Switching the Light ON (AUTO mode)

If a trigger condition occurs and the DARK input is high, the light will be switched on. The light's brightness will increase to the selected brightness within the selected fade time.

The RELAY output is activated at the start of the fadingon cycle. The light and the relay will remain on for the duration set by the ON-TIME input.

Conditions for Switching the Light OFF (AUTO mode)

The light is switched off softly after the selected ON-TIME has elapsed, or if the DARK input senses a low voltage. The light's brightness will reduce to zero within the selected fade time. The RELAY output is switched off at the start of the fading-off cycle.



DevicePinOut_

Pin No.	Name	Description
1	BRIGHT	Brightness adjustment
2	FADE	Fade time adjustment
3	SENS	Sensitivity threshold adjustment
4	ON-TIME	Light on-time adjustment
5	TCOMP	Temperature compensation input
6	DARK	Dark mode input, connected to LDR/Photodiode
7	SWIN	ON-AUTO-OFF select input
8	LMODE	LED mode select
9	VB	Supply voltage input
10	VDDA	Analog supply
11	TEST	Reserved test mode, connect to VSS
12	TTEST	Reserved test mode (TOG), connect to VSS
13	RETRIG	Retrigger mode select input
14	PIRIN	PIR sensor input
15	VSSA	Analog ground
16	VSS	Digital ground
17	RELAY	Relay output pin
18	PWM	Light output (PWM)
19	VDD	Digital VDD
20	VSW	Voltage sense input

Table 6: Device Pin Out

Component Values_

Designator	Description
R1	33k
R2	20k
R3	22k
R4	180k
R5*	22k
R6*	1k to 50k, load dependent
P1,P2, P3, P4	47k
C1, C4	470nF
C2	1nF
C3	10µF
IRA1	LHI 878, Perkin Elmer
Q1*	NPN
N1	NTC 47k
LDR1*	Light dependent resistor
P5*	2.2M Trim pot

Table 7: Component Values for Application Circuit (*Suitable component values must be determined by the manufacturer)

Contact Information

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Ordering Information_

E910.99 SO20W (Surface mount, 300 mills) E910.90 DIE (Unpackaged devices)